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Docket No. 500.41297CX1
Serial No. 10/630,706
Office Action dated April 4, 2006**AMENDMENTS TO THE CLAIMS**

The following listing of claims replaces all prior listings, and all prior versions, of claims in the application.

LISTING OF CLAIMS:

1. (Currently Amended) An image display apparatus comprising:
 - a plurality of scanning wires arranged in an image display region for transmitting a scanning signal;
 - a plurality of signal wires arranged to intersect with said plurality of scanning wires in said image display region for transmitting a signal voltage;
 - a plurality of current driven electro-optical display elements each arranged in a pixel region surrounded by said scanning wires and said signal wires connected to a common power supply;
 - a plurality of driving elements arranged in said pixel region connected with said electro-optical display elements; and
 - a plurality of memory control circuits each including a sampling switch and a driving switch for holding said signal voltage in response to said scanning signal and to control driving of said driving elements based on said held signal voltage,
wherein said memory control circuit samples and holds said signal voltage while blocking a bias voltage from being applied to each of said driving elements by closing said sampling switch and opening said driving switch, and subsequently applies said held voltage signal to said driving elements with said held signal voltage as said bias voltage by opening said sampling switch and closing said driving switch.
2. (Previously Presented) An image display apparatus according to claim 1, wherein a power supply control element stops supplying the electric power to said driving elements.

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3. (Original) An image display apparatus according to claim 1, wherein said memory control circuit comprises:

a main driving switch element responsive to said scanning signal to conduct for sampling said signal voltage; and

a sampling capacitor for holding the signal voltage sampled by said main sampling switch element.

4. (Original) An image display apparatus according to claim 1, wherein said memory control circuit comprises:

a main driving switch element responsive to said scanning signal to conduct for sampling said signal voltage;

a sampling capacitor for holding the signal voltage sampled by said main sampling switch element; and

an auxiliary driving switch element responsive to said scanning signal to conduct for connecting one end of said sampling capacitor to a common electrode.

5. (Original) An image display apparatus according to claim 1, wherein said current driven electro-optical display elements comprise organic LEDs.

6. (Currently Amended) An image display apparatus comprising:

a plurality of scanning wires arranged in an image display region for transmitting a scanning signal;

a plurality of signal wires arranged to intersect with said plurality of scanning wires in said image display region for transmitting a signal voltage;

a plurality of current driven electro-optical display elements arranged in a pixel region surrounded by said scanning wires and said signal wires connected to a common power supply;

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a plurality of driving elements arranged in said pixel region connected with said electro-optical display elements; and

a plurality of memory control circuits each including a sampling switch and a driving switch for holding said signal voltage in response to said scanning signal and to control driving of said driving elements based on said held signal voltage,

wherein said memory control circuit samples and holds said signal voltage in said sampling period by closing said sampling switch and opening said driving switch; and

wherein a voltage applied to said driving elements in a sampling period is lower than a voltage in a write period.

7. (Original) An image display apparatus according to claim 6, wherein said driving elements are non-conductive in said sampling period.

8. (Original) An image display apparatus according to claim 6, wherein said memory control circuit comprises:

a main driving switch element responsive to said scanning signal to conduct for sampling said signal voltage; and

a sampling capacitor for holding the signal voltage sampled by said main sampling switch element.

9. (Original) An image display apparatus according to claim 6, wherein said memory control circuit comprises:

a main driving switch element responsive to said scanning signal to conduct for sampling said signal voltage;

a sampling capacitor for holding the signal voltage sampled by said main sampling switch element; and

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an auxiliary driving switch element responsive to said scanning signal to conduct for connecting one end of said sampling capacitor to a common electrode.

10. (Original) An image display apparatus according to claim 6, wherein said current driven electro-optical display elements comprise organic LEDs.

11. (Currently Amended) An image display apparatus comprising:
a plurality of scanning wires arranged in an image display region for transmitting a scanning signal;
a plurality of signal wires arranged to intersect with said plurality of scanning wires in said image display region for transmitting a signal voltage;
a plurality of current driven electro-optical display elements arranged in a pixel region which is surrounded by said scanning wires and said signal wires connected to a common power supply;
a plurality of driving elements arranged in said pixel region connected with said electro-optical display elements;
a plurality of memory control circuits each including a sampling switch and a driving switch for holding said signal voltage in response to said scanning signal and to control driving of said driving elements based on said held signal voltage;
a power supply control element for controlling electric power supplied from said common power supply to said driving elements,
wherein said memory control circuit samples and holds said signal voltage in said sampling period by closing said sampling switch and opening said driving switch; and
the electric power supplied to said driving elements in a sampling period is lower than the electric power in a write period.

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12. (Original) An image display apparatus according to claim 11, wherein said memory control circuit comprises:

a main driving switch element responsive to said scanning signal to conduct for sampling said signal voltage; and

a sampling capacitor for holding the signal voltage sampled by said main sampling switch element.

13. (Original) An image display apparatus according to claim 11, wherein said memory control circuit comprises:

a main driving switch element responsive to said scanning signal to conduct for sampling said signal voltage;

a sampling capacitor for holding the signal voltage sampled by said main sampling switch element; and

an auxiliary driving switch element responsive to said scanning signal to conduct for connecting one end of said sampling capacitor to a common electrode.

14. (Original) An image display apparatus according to claim 11, wherein said current driven electro-optical display elements comprise organic LEDs.